Electronic Patent Application Fee Transmittal							
Application Number:	10	10823158					
Filing Date:	13-Apr-2004						
Title of Invention:	METHODS AND STRUCTURES FOR PLANAR AND MULTIPLE-GATE TRANSISTORS FORMED ON SOI						
First Named Inventor/Applicant Name:	Fu	Fu-Liang Yang					
Filer:	Ira Stuart Matsil/Kristin Hayes						
Attorney Docket Number:	TSM03-0929						
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Basic Filing:							
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